Form 1449 (Modified)	Atty Docket No. RATLP001H
Information Disclosure Statement By Applicant	Application No.: 10/613,516
	Inventor REED HASTINGS
	Group Unassigned
(Use Several Sheets if Necessary)	Filing Date July 3, 2003
	Mailing Date February 2, 2004

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
PW	1	3,815,103	06/1974	Holtley et al.	340	172.5	
PW	2	4,104,718	08/1978	Poublan et al.	364	200	
PW	3	4,394,731	07/1983	Flusche et al.	395	472	
PW	4	4,533,997	08/1985	Furgerson	385	425	
PW	5	4,802,165	01/1989	Ream	371	19	
PW	6	4,811,347	03/1989	Bolt	371	51	
PW	7	4,815,025	03/1989	Ossfeldt et al.	364	900	
PW	8	4,903,194	02/1990	Houdek et al.	364	200	
PW	9	4,937,736	06/1990	Chang et al.	364	200	
PW	10	4,953,084	08/1990	Meloy et al.	364	200	
PW	11	5,025,366	06/1991	Baror	364	200	
PW	12	5,029,078	07/1991	Iwai	364	200	
PW	13	5,075,842	12/1991	Lai	395	425	
PW	14	5,115,499	05/1992	Stiffler et al.	395	425	
PW	15	5,132,972	07/1992	Hansen	371	19	
PW	16	5,175,837	12/1992	Arnold et al.	395	425	
PW	17	5,193,180	03/1993	Hastings	395	375	
PW	18	5,230,070	07/1993	Liu	395	425	
PW	19	5,335,344	08/1994	Hastings	395	375	
PW	20	5,404,499	04/1995	Nichols	364	266	
PW	21	5,412,788	05/1995	Collins et al.	395	425	
PW	22	5,438,670	08/1995	Baror et al.	395	403	
PW	23	5,528,753	06/1996	Fortin	395	183.11	

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation Yes	No
PW	24	0496494	07/1992	EPO				

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
PW	25	Anderson, "C customized memory allocators", Winter 1989/1990, Dr. Dobb's C Sourcebook, pp. 62-66, 94.
PW	26	Aral et al., "Efficient debugging primitives for multiprocessors", 1989, Encor Computer Corporation, ASPLOS III Proceedings, SIGPLAN Notices, Vol. 24, pp. 87-93.
PW	27	Austin et al., "Efficient detection of all pointer and array access errors" 1993, Computer Sciences Dept., Univ. of Wisconsin-Madison, pp. 1-29.
PW	28	Bishop, "Profiling under UNIX by patching", 1987, Software Practice and Experience, Vol. 17, No. 10, pp. 729-739.
PW	29	Boothe, "Fast accurate simulation of large shared memory multi-processors", 1988, US Berkeley EECS Rpt. No. UCB/CDS 92/682.

1-18-07
2rm